



ASSAM UNIVERSITY :: SILCHAR

Department of Computer Science & Engineering, TSSOT, Assam University, Silchar

MID TERM EXAMINATION (Odd - 2022)

TENTATIVE SCHEDULE

Date (Day)		Shift-1 9:30 - 11:00	SHIFT-2 11:30 - 1:00	SHIFT-3 1:30-3:00	SHIFT-4 3:30-5:00
17/12/2022 (Saturday)	B.Tech	ASH 101 Engineering Physics SOT CF 02 Engineering Physics	CSE 502 Design & Analysis of Algorithms	ASH 301(C) Mathematics -III CSECC 02 Discrete Mathematics & Graph Theory	CSE701(4)/ (5) / (8) Neural Networks/ Speech and Natural Language Processing/ Mobile Computing
	M.Tech	MCSE 101 Mathematical foundations of Computer Science			
19/12/2022 (Monday)	B.Tech	ASH 102 Mathematics -I SOT CF 01 Engineering Mathematics	CSE 503/ CSECC 15 Operating Systems	ASH 302 Effective Technical Communication	
	M.Tech	MCSE 102 Advanced Data Structures			
20/12/2022 (Tuesday)	B.Tech	ASH 103 English-I SOTCF 04 Communication Skill in English	CSE 504/ CSECC 17 Formal Language & Automata Theory	CSE 301 Analog Electronic Circuits	CSE702(4) / (11) Soft Computing/ Image Processing
	M.Tech	MCSE 103 Soft Computing			
21/12/2022 (Wednesday)	B.Tech	SOTCF03 (Environmental Science)	CSE505(6/12) Data Mining/ Computer Graphics CSECC 18 Computer Graphics and Multimedia	CSE 302/ CSECC 01 Data Structure	
	M.Tech	MCSE 104(A)/ (B) Advanced Operating Systems / Natural Language Processing			
22/12/2022 (Thursday)	B.Tech	SOTCF05 (Engineering Chemistry)	ASH 501 Mathematics-IV Numerical Analysis	CSE 303/ CSECC 03 Digital Electronics	CSE703(32) Cryptography & Network Security
	M.Tech	MCSE 105 Research Methodology and IPR			MCSE 301(C) / (D) Deep Learning/ Internet of Things
23/12/2022 (Friday)	B.Tech	SOT EF 01 Economics and Human Development		CSE 304 Microprocessor CSECC 16 Microprocessor and Microcontroller	Open Elective: AE 704 Environmental Engineering
	M.Tech				MCSE 303(E) Web and Internet Technology

This is in partial modification of our earlier programme Notification no. AUE/SOT/MID-SEM/2016 dtd. 15.11.2022.

(Dr. S. Dutta Roy)

Controller of Examinations

No. : AUE/SOT/Mid-Sem/2016

Date : 25.11.2022

Copy to :

1. Dean, School of Technology for information and necessary action.
2. Head, Deptt. of Computer Science & Engineering, AUS for information and necessary action.
3. Director, Computer Centre for uploading in University Website.
4. System Analyst, AUS for uploading in Examination portal.
5. PS to VC for kind information of the Vice Chancellor.

Controller of Examinations